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In re Application of:

Cyprian E. UZOH et al.

Serial No.: 09/611,955 Filed: July 6, 2000

Attorney Docket No.: F19-97-205US2

**DECISION ON PETITION** 

This is a decision in response to the communication filed June 3, 2004. The communication has been treated as a petition under 37 C.F.R. § 1.181, requesting reconsideration of the objection to the specification, drawing and claims for new matter. No petition fee is required.

## The petition is **GRANTED**.

In support of the petition, it is asserted that "[t]he specification as originally filed explicitly discloses that recesses are formed in at least one major surface of the semiconductor substrate. For instance see page 1, lines 14-16 of the original disclosure that state: '[t]his is achieved by selectively plating recesses in a semiconductor substrate with conductive metal such as copper or gold.' Also, see original claim 25 as filed which states 'recesses located in at least one major surface of said semiconductor substrate.' In addition, see the original Abstract of the Disclosure that states: 'Recesses in a semiconductor structure are selectively plated...' Accordingly, the above recitation is not new matter and claims 25 and 32 properly recite 'in.'"

Upon further considering the original disclosure, particularly original claim 25 which contains the limitation "recesses located in at least one major surface of said semiconductor substrate; electrical insulating layer over said at least one major surface and in said recesses," there is reasonable support therein for the Amendment filed July 31, 2004. Therefore, the amendments to the specification, drawings and claims do not contain new matter.

The application is being forwarded to the examiner for further consideration of the instant application in light of this decision.

James L. Dwyer Director Technology Certer 2800

Semiconductors, Electrical and Optical Systems and Components